

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1 Claims 1-16 (Canceled).

1 Claim 17 (previously presented): A digital camera according to claim 23, wherein said
2 memory is an SDRAM, and said writer includes a buffer for holding the processed image data output
3 from said processor, and a transferor for transferring to said memory the processed image data held
4 by said buffer.

1 Claim 18 (previously presented): A digital camera according to claim 23, wherein said
2 memory has a plurality of memory areas, said digital camera further comprises a changer for
3 changing a selecting of a memory area at an interval of the first time period, and wherein said writer
4 writes the processed image data to one of said plurality of memory areas based on a changing result
5 of said changer, and said reader reads the processed image data from another of said plurality of
6 memory areas based on the changing result of said changer.

1 Claim 19 (previously presented): A digital camera according to claim 18, wherein said
2 changer changes the selecting of the memory area in a predetermined order.

1 Claim 20 (previously presented): A digital camera according to claim 18, wherein the
2 number of the memory areas is two, and the second time period is $\frac{1}{2}$ of the first time period.

1 Claim 21 (previously presented): A digital camera according to claim 23, wherein the
2 second time period is one over an integer of the first time period.

1 Claim 22 (previously presented): A digital camera according to claim 23, wherein said
2 recorder records to said record medium the processed image data in a compressed state.

1 Claim 23 (currently amended): A digital camera, comprising:
2 an imaging device having an imaging surface which generates an image signal corresponding
3 to an optical image of an objective scene;
4 a processor for subjecting the image signal generated by said imaging surface to signal
5 processes including a thinning process performed by a thinning-out circuit so as to create processed
6 image data at a rate of one screen per a first time period;
7 a memory having a single input/output port;
8 a writer for writing to said memory the processed image data output from said processor;

9 a reader for reading the processed image data stored in said memory at a rate of one screen
10 per a second time period which is shorter than the first time period;

11 a displayer for displaying an image based on the processed image data read out by said
12 reader;

13 a first instructor for instructing said processor to suspend the thinning process at a time of
14 accepting a recording operation; [[and]]

15 a recorder for recording to a record medium the processed image data stored in said memory
16 in response to the recording operation, and further comprising a second instructor for instructing said
17 reader to suspend a reading process in association with an instructing process of said first instructor;

18 a buffer in communication with said memory;

19 a first switch; and

20 a shutter button, wherein said first switch disconnects said thinning-out circuit from said
21 buffer to disable said thinning-out circuit when said shutter button is operated.

1 Claim 24 (new): A digital camera according to claim 23, further comprising:

2 an NTSC encoder in communication with said displayer;

3 a black image generating circuit for supplying black image data; and

4 a second switch,

5 wherein said second switch connects said black image generating circuit to said NTSC
6 encoder to supply the black image data to said NTSC encoder and display a black image on said

7 displayer when said shutter button is operated,

8 wherein the writing to said memory is suspended when the first time period has elapsed from
9 the operating of said shutter button.

1 Claim 25 (New): A digital camera, comprising:

2 an imaging device having an imaging surface which generates an image signal corresponding
3 to an optical image of an objective scene;

4 a processor for subjecting the image signal generated by said imaging surface to signal
5 processes including a thinning process so as to create processed image data;

6 a memory having a single input/output port;

7 a writer for carrying out a writing process to write to said memory the processed image data
8 created by said processor at a rate of one screen per a first time period;

9 a reader for carrying out, in parallel with the writing process, a reading process to read the
10 processed image data stored in said memory at a rate of one screen per a second time period which
11 is shorter than the first time period;

12 a displayer for carrying out, in parallel with the reading process, a displaying process to
13 display an image based on the processed image data read out from said memory;

14 a first instructor for instructing said processor and said reader to suspend the thinning process
15 and the reading process, respectively, at a time of accepting a recording operation;

16 a second instructor for instructing said writer to suspend the writing process at a timing of

17 storing in said memory specific processed image data created by said processor after an instructing
18 process of said first instructor; and
19 a recorder for recording to a record medium the specific processed image data stored in said
20 memory.

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